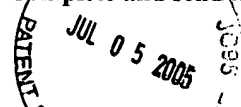


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07/08/2005 FFANAIA3 00000030 041679 10694676

01 FC:1501 1400.00 DA
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/694,676	10/28/2003	Tao-Ping Wang	TS02-1089	9642

TITLE OF INVENTION: HIGH EFFICIENCY REDUNDANCY ARCHITECTURE IN SRAM COMPILER

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/07/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
PHAN, TRONG Q	2827	365-200000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Taiwan Semiconductor Manufacturing Co., Ltd.

TAIWAN 300-77, R.O.C.

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Steven E. Koffis

Date

July 5, 2005

Typed or printed name

Steven E. Koffis

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37,163

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